Patent Search

Invention Title	A NEW SOFTWARE BASED ALGORITHM IMPLEMENTATION FOR DYNAMIC TESTING OF ANALOG TO DIGITAL CONVERTER (ADC) USING HISTOG TECHNIQUE WITH SINE WAVE, TRIANGULAR WAVE AND APPLICATION MODE AS INPUT SIGNALS.
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Abstract:

A NEW SOFTWARE BASED ALGORITHM IMPLEMENTATION FOR DYNAMIC TESTING OF ANALOG TO DIGITAL CONVERTER (ADC) USING HISTOGRAM TECHNIQUE WITH SINE W TRIANGULAR WAVE AND APPLICATION MODE AS INPUT SIGNALS. ABSTRACT: ADC is an important device widely used in electronics, communication and instrumentation s biomedical signal processing, military applications for interfacing Analog devices with digital devices. All the existing testing methods are sophisticated, costly and time consuming, every people cannot effort. It is a dream of every young researchers and designer that if the method tests the devices and determining the design parameters error, Offset error, Differential nonlinearity (DNL), Integral nonlinearity (INL), Effective Number of Bits (ENOB) etc [1-3]. In case of high volume production of Integrated Circ (ICs), manufacturing costs are strongly affected by testing costs. Hence ADC design and testing is an important activity which plays main role in deciding accuracy of a syste Here we have proposed the software based a novel testing algorithm implementation for dynamic testing of analog to digital converter (ADC) using Histogram test techniq sine wave, triangular wave and application mode as input signal for high speed data converting applications. This proposed algorithm is time saver, error corrector and us friendly.

Complete Specification

FORM 2
THE PATENTS ACT 1970
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THE PATENT RULES 2003
COMPLETE SPECIFICATION
(SEE SECTIONS 10 & RULE 13)
1. TITLE OF THE INVENTION
A NEW SOFTWARE BASED ALGORITHM IMPLEMENTATION FOR DYNAMIC TESTING OF ANALOG TO DIGITAL CONVERTER (ADC) USING HISTOGRAM TECHNIQUE WITH SINE WAVE, TRIANGULAR WAVE AND APPLICATION MODE AS INPUT SIGNALS.
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