Department of Electronics & Telecommunication Engineering

05/12/2022

Minutes of the DPAQIC Meeting

Meeting Departmental Program Assessment & Quality Improvement Committee (DPAQIC) was held on 05th December 2022 in hybrid(Online/Offline) mode.

Following members attended the meeting:

Sr. No.	Name	Designation	
1.	Dr. (Mrs.) Anjana Jain	HOD E&TC	Chairman
2.	Dr. P.P Bansod	Professor & HOD BME,	Senior Professor from
	(Attended online)	SGSITS	allied dept.
3.	Dr. Ravi Sindal	Professor, E&TC Dept, IET	Senior professor from
	(Attended online)	DAVV	nearby Academic Institute
4.	Mr. Nitin Poddar	MD, Telexa Tech. Pvt. Ltd.,	Expert from Industry
	(Attended online)	Indore (Alumni)	
5.	Mrs. S.V. Charhate	Professor, E&TC	Member
6.	Dr.Shekhar Sharma	Professor, E&TC	Member
7.	Dr.(Mrs.) Anjulata Yadav	Associate Professor, E&TC	Member
8.	Dr. S.K Jain	Associate Professor, E&TC	Member
9.	Dr.L.D. Malviya	Associate Professor, E&TC	Member
10.	Dr. Preeti Trivedi	Associate Professor, E&TC	Member
11.	Mr. Manish Panchal	Associate Professor, E&TC	Member
12.	Mr. Amit Naik	Associate Professor, E&TC	Member
13.	Mrs. Rekha Jain	Assistant Professor, E&TC	Member
14.	Mrs. Jaya Dipti Lal	Assistant Professor, E&TC	Member
15.	Mr. Ashwin Shrivastava	Assistant Professor, E&TC	Member
16.	Mr. Ajay Parmar	Assistant Professor, E&TC	Member
17.	Mr. Shubham Srivastava	Assistant Professor, E&TC	Member
18.	Ms. Deepali Kothari	Assistant Professor, E&TC	Member
19.	Mrs. Ritika Nair	Assistant Professor, E&TC	Member
20.	Mr. Mohit Khamele	Assistant Professor, E&TC	Member
21.	Mr. Neeraj Malviya	Assistant Professor, E&TC	Member
22.	Ms. Shruchi Jain	Assistant Professor, E&TC	Member
23.	Ms. Neeta Kothari	Assistant Professor, E&TC	Member

Following are the deliberations of the meeting.

- 1. Minutes of the Previous DPAQIC meeting held on 07/06/2022 were confirmed.
- 2. As per the NEP-2020 the Schemes of B.Tech II, III & IVth Year were proposed & discussed in the meeting. Scheme of IIInd year is approved while scheme of III & IVth are tentatively approved. Changes suggested by experts are incorporated in the scheme.

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3. It was suggested that core subjects should not be compromised in the new scheme. Changes in the Syllabus of a few subjects of the new scheme were suggested and concerned faculty members were asked to prepare the same.

4. In the existing scheme of III year E&TC engg. Credit of Electronics Design & Simulation lab (EC35881) is proposed to be changed from 2 to 1 from session 2023-24 onwards

5. Scheme & Syllabus of M.Tech Electronics & Telecommunication Engineering is unchanged.

1. Prof. Anjana Jain

2. Prof. P.P Bansod

3. Prof. Ravi Sindal

4. Mr. Nitin Poddar

5. Prof. Shekhar Sharma