





# **One-week Training Program On**

# "Advanced Approaches in Modelling and Simulation for Cutting-Edge Electronic Systems"

12-18 June, 2023

Organized by

Department of Electronics & Instrumentation Engineering, SGSITS, Indore, M.P.

in association with

## Banasthali Vidyapith, Rajasthan

under

SYNERGISTIC TRAINING PROGRAM UTILIZING THE SCIENCE & TECHNOLOGY INFRASTRUCTURE (STUTI)

Department of Science & Technology (DST), Govt. of India

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#### About STUTI program

The Scheme 'Synergistic Training program Utilizing the Scientific and Technological Infrastructure' (STUTI) is intended to build human resource and its knowledge capacity through open access S&T Infrastructure across the country. As a complement to the various schemes of DST funding for expansion of R&D Infrastructure at academic institutions, STUTI scheme envisions a hands-on training program and sensitization of the state-of-the-art equipment as well as towards sharing while ensuring transparent access of S&T facilities.

## Topics to be covered:

- ✓ HDL (VHDL/Verilog).
- ✓ FPGA based design.
- ✓ MATLAB (Simulink).
- ✓ Simulation & modelling using CADENCE.
- ✓ Interfacing of I/O devices with industrial grade Microcontrollers.
- Introduction to IoT.

#### Registration and Other information:

#### Application Deadline: 8 June, 2023

- Interested Candidates need to register via the given link. Only 40 Candidates are allowed to attend the training program.
- Only 3 candidates are allowed from the same institute.
- Selected candidates will be informed via registered email.
- Lodging and boarding will be provided by the host institute.
- TA: 2nd class sleeper Train or Bus fare as per rules of DST, Govt. of India.

#### Registration Link:

https://forms.gle/mcrCgr9DQBiWBpTX

FREE REGISTRATION SCAN OR CODE



## Eligibility:

Faculty members, Doctoral and Postdoctoral students, Scientists, Post graduate students of basic Sciences, Electronics, Electrical and its allied branches of Engineering







