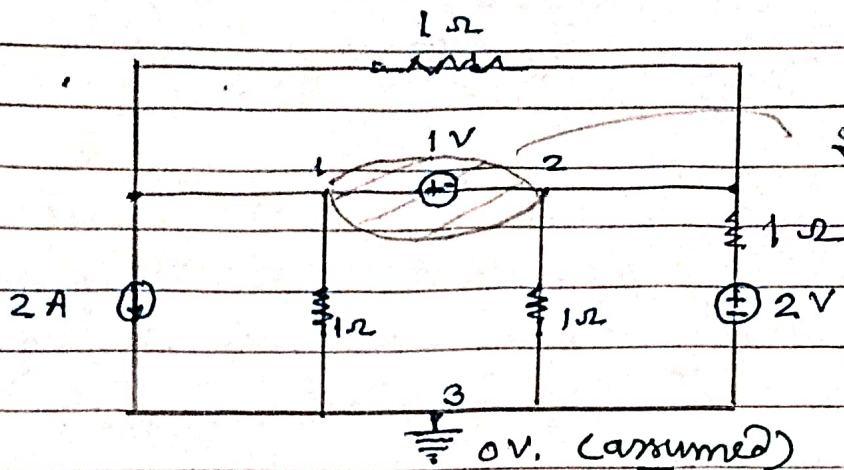


SUPER NODE

Find V_1 and V_2



Super node

Considering simple node which is imaginary, called Super node

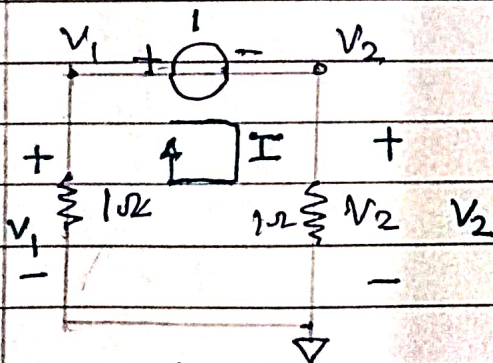
- one ref. node considered. (1, 2, 3).
- KCL eq. for each non ref. node
- solve eq. to get unknown voltages.
- supernode made from (1-2) including source.

KCL at V_1 .

$V_3 = 0V$.

$$2 + \frac{V_1 - 0}{1} + \frac{V_2 - 0}{1} + \frac{V_2 - 2}{1} = 0$$

$$V_1 + 2V_2 = 0 \quad \text{--- (1)}$$



from above fig.

$$V_1 - 1 - V_2 = 0$$

see loop current,

$$V_1 - V_2 = 1 \quad \text{--- (2)}$$

Let eq. (1) & (2)

$$V_1 + 2V_2 = 0$$

$$-V_1 + V_2 = 1$$

$$3V_2 = 1$$

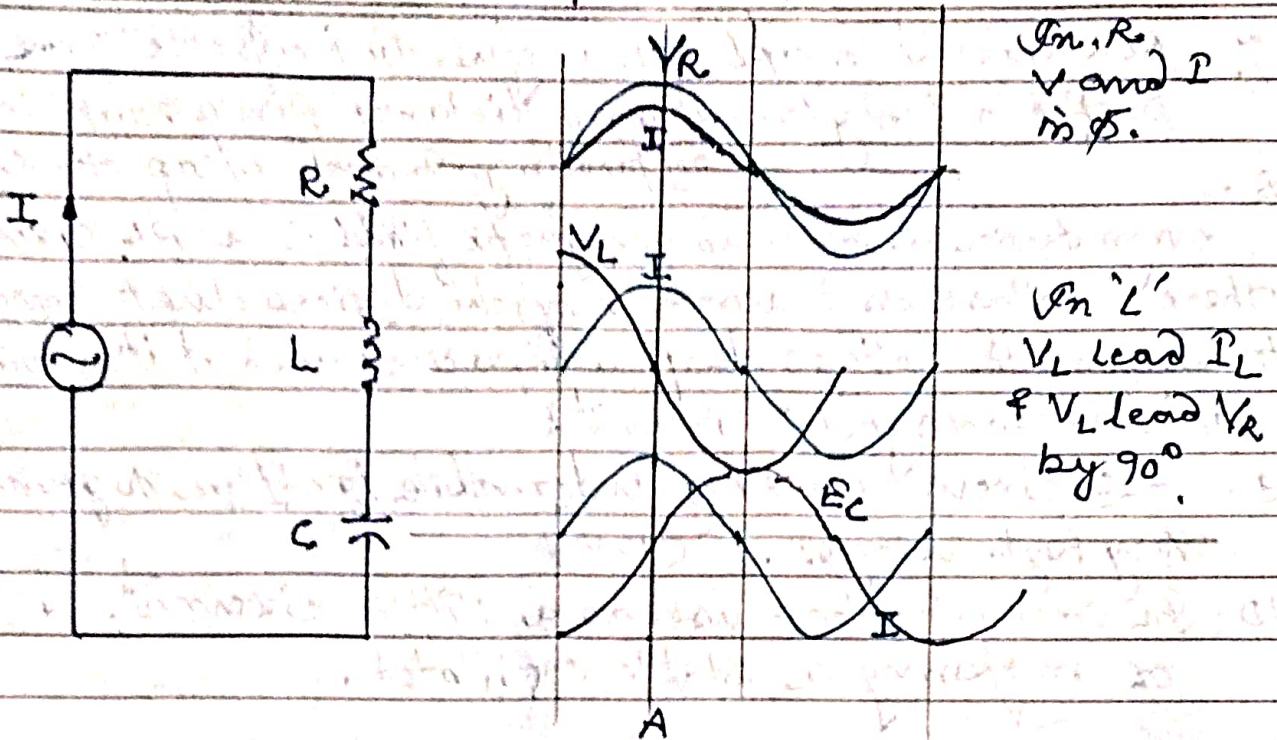
$$V_2 = \frac{1}{3}$$

$$\& V_1 = 1 + V_2 \quad \text{--- eq (2)}$$

$$= 1 + \left(\frac{1}{3}\right)$$

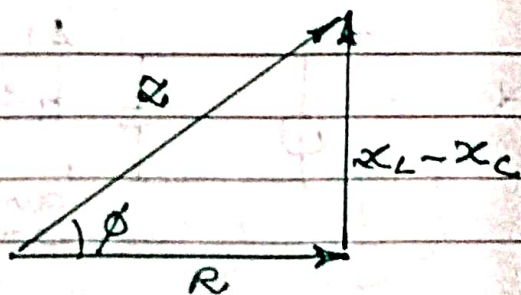
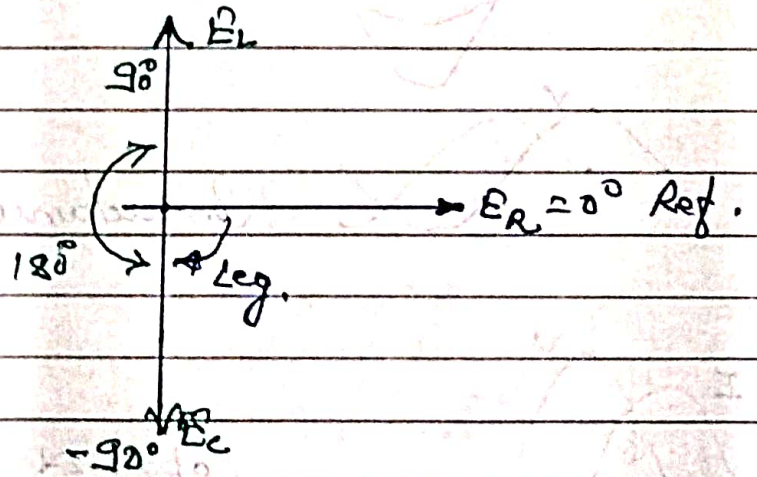
$$= \frac{4}{3} \text{ Volt.}$$

Sinusoidal Response RLC Series circuit.



Let see at point A at which E_R, I_R max.
for
at Point A. V_L Lead V_R
by 90° .

at point A, V_C Lag V_R
by (-90°) or $\text{Lag}(-90^\circ)$
means.



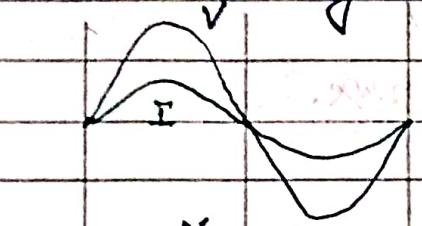
$$Z^2 = R^2 + (X_L - X_C)^2$$

$$\tan \phi = \frac{X_L - X_C}{R}$$

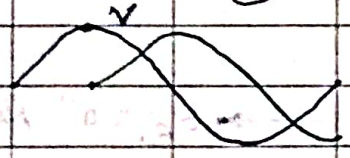
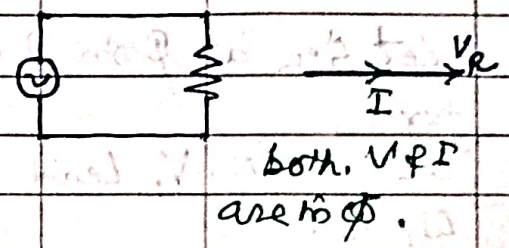
- Application :
1. C controls current in line by X_C
 2. X_L control voltage in electric circuit.
 3. If X_L and X_C known we can determine V_C & V_L .
 4. If phase ϕ known we can draw complete phasor diagram.
 5. Useful in good grounding system to dodge faulty current.
 6. CFL bulb RLC circuit used to improve power factor.
Hence reducing power consumption.

AC circuit

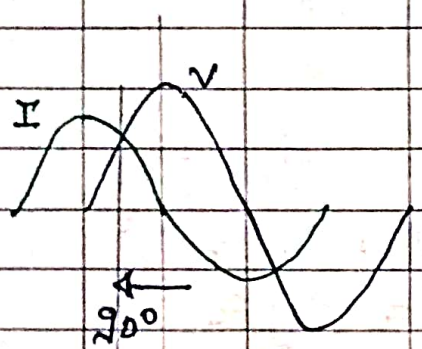
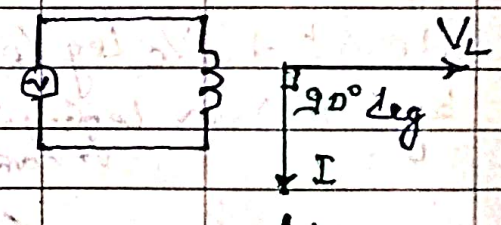
7. RLC circuit concept are useful to improve good voltage profile in Long/medium distance power transmission.
8. by providing Inductance/capacitance compensation.
- every device which used magnetic field is a RL circuit.
- there is voltage drop across magnetic devices due to mag. effect, to compensate voltage drop a capacitor used at its terminal.
- so it becomes RLC circuit.
9. RLC circuit used by automobile ignition to generate very high voltage. (12V)
10. In communication. used as a Filter circuit.
- or making a stable oscillator.



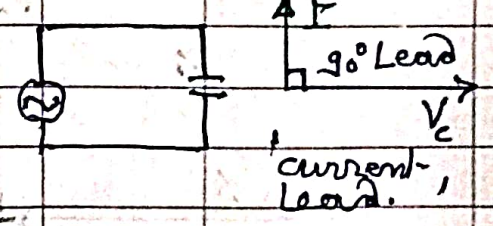
Resistance



Inductance

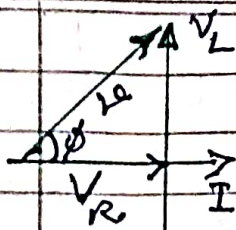


capacitor



YouTube . explainer SERIES RL CIRCUIT

current eq.
power eq.
power plot.



phasor.

$$V = V_R + jV_L$$

Here V_L lead by 90° The circuit current.

$I = I_m \sin(\omega t - \pi/2)$ for L excited by V .

for RL circuit, $i = I_m \sin(\omega t - \phi)$

for voltage across R,

$$i = I_m \sin \omega t.$$



From phasor diagram.

$$V^2 = V_L^2 + V_R^2$$

$$= (IR)^2 + (IX_L)^2$$

$$V^2 = I^2(R^2 + X_L^2)$$

$$V = I \sqrt{(R^2 + X_L^2)}$$

$$I = \frac{V}{\sqrt{(R^2 + X_L^2)}} = \frac{V_m \sin \omega t}{R^2 + X_L^2}$$

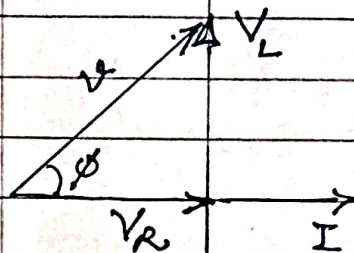
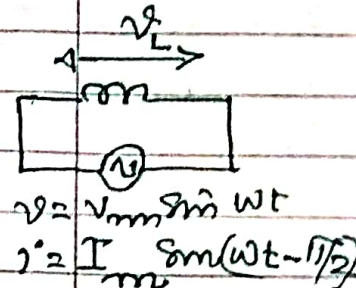
$$I = \frac{V_m}{Z}$$

Let we have.

$$i = I_m \sin(\omega t - \phi)$$

$$= I_m \sin(\omega t - \frac{X_L}{\tan^{-1} X_R})$$

$$= \frac{V_m}{\sqrt{R^2 + X_L^2}} \sin(\omega t - \tan^{-1} \frac{X_L}{X_R})$$



$$\tan \phi = \frac{V_L}{V_R}$$

$$= \frac{IX_L}{IX_R}$$

$$\tan \phi = X_L / X_R$$

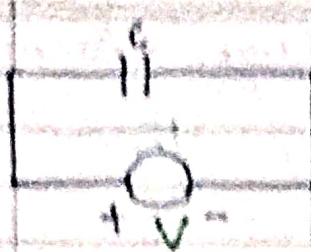
Let $\sin \omega t = 1$

RC CIRCUIT

$$V_c = \frac{1}{C} \int i dt$$

$$i = C \frac{dV_c}{dt}$$

If cap. fully charged.
flow of any more electrons
onto its plates, as it becomes
saturated.



when ac applied to
circuit, it holds voltage
across it.
Here current is opposed by
the any changes in the
voltage at a rate which is
equal to the rate of change
of the electrom. charges in
the plates.

AC Through capacitor.

ac. circuit cap. constantly charging & discharging. This
± changes in saturation of voltage takes time to deposit
or release charges.

$I = \text{max.}$

case I.
S.C.

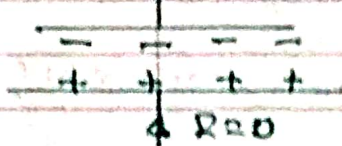


case-II.

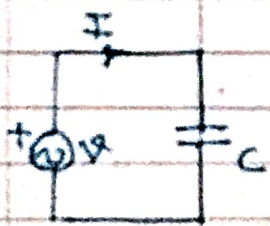
case III

$I(\text{max})$

polarity reversed.



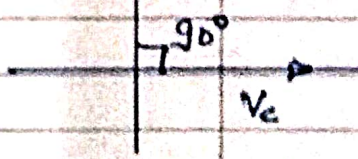
case IV.



$$V = V_m \sin \omega t$$

$$i_c = C \frac{dV_c}{dt}$$

I_c



rate of change
 V_c across the plates
is known at its max.
 i_c = also maximum.

Hence Max. amount of
electrons are moving
from one plate to another
 I_c phase Lead V_c phase
when plates saturated no current
flows.