Shri G.S. Institute of Technology & Science, Indore

Department of Electronics & Instrumentation Engineering

DPAQIC Meeting & Its Agenda

Date: 1/1/2025

Circular

The meeting of DPAQIC in Electronics & Instrumentation Department is scheduled on 7th Jan 2025 at 3 PM in the conference room of the department. It is requested to make it Convenient to attend the meeting. The agenda of the meeting is as given below:

1. To review the action taken report (ATR) of program/ course attainments for year 2023-24.
2. Suggestions on feedback analysis of PO by stake holders (Exit survey & External Examiner)
3. Incorporations / suggestions for overall upliftment of the department in future.
4. Suggestions on Faculty feedback & improvements.
5. To review & suggestion on submissions for IQAR & NAAC.
6. Any other point with the permission of chair.

01/01/2025

Dr. Rajesh Khatri
Asso. Professor & Head
Chairman, DPAQIC

Copy to-
The Director, SGSITS, Indore
The Dean (Academics),SGSITS, Indore
All Members of DPAQIC
DPAQIC File, EID

SHRI G.S. INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics & Instrumentation Engineering

Minutes of Meeting DPAQIC

7/01/2025

A meeting of DPAQIC of the department was held on 7th Jan 2025. The deliberations of the meeting are as under:

1 The action taken report (ATR) for the academic year 2023-24 was reviewed & approved by the DPAQIC members.

2. The feedback analysis of program outcomes (POs) by stakeholders (Exit survey, External Examiners, Alumni) was discussed. Dr. P.P. Bansod has suggested to develop an in-house proforma for feedback from recruiters and provide them during recruiting departmental students.

3. The courses outcomes (COs) for new courses offered were discussed and revised (as per suggestion)

4. The scheme for minor degree has been discussed & suggestions are incorporated and forwarded for BoS approvals.

5. For maintaining uniformity in scheme (following across different branches in the institute), a tentative scheme has been proposed and discussed. The concluded suggestion are forwarded to BoS. Suggestions are summarized as

a) Merging the Laboratory for digital electronics with Electronics workshop Lab (Skill Lab-I)

b) The skill Lab-II may include scripting language along with software laboratory.

1) Dr. Rajesh Khatri — 7/1/25

2) Dr. P. P. Bansod

3) Dr. Shekhar Sharma.

4) Dr. Vaibhav Neema (External Expert) — 07/01/25

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DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGG.

ACTION TAKEN REPORT 2023-24

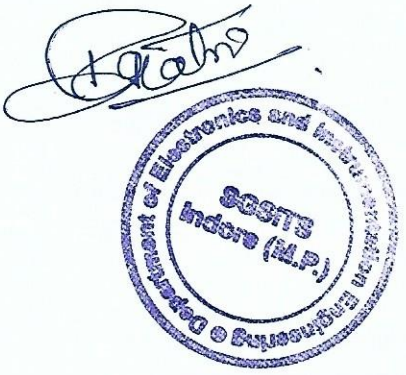
Following actions are taken on the basis of feedback from various stakeholders (Students- Exit survey (Batch 2020-2024), External Examiner) and the ATR report of previous academic year:

1. New course "Digital Circuit Design using HDL" with Theory and Practical of total 3 credits is introduced in IV semester. The course is designed such that is focuses on skill development and employability. It also includes practical hands-on on FPGA boards using HDL that develops understanding and use of modern tool. The course is also designed considering the new NEP 2020 policy.
2. Two new Elective courses of NPTEL in VIII semester are introduced which are in line with the already included courses. "Automation and Control" of NPTEL which is in line with "Automation in Instrumentation EI47776" and "Biomedical Signal Processing" of NPTEL that is in line with "Medical Instrumentation-BM47001.
3. Taking immediate action to improve the drinking water facility in the department we have Water Purifier and Chiller with adequate and pure water supply.
4. To motivate the students for self-development and to make them familiarize with the industry demands the concept of "Alumni lecture series" is introduced where our alumni will interact with the students (online or offline mode) and share their experience.

PROPOSED ACTION PLAN

Considering the Exit survey from the students of batch 2023-24 shows a considerable increase in the overall average values of program outcomes but the from the feedback of External examiners following actions are suggested to be taken in near future:

1. Improved quality of major projects where students can apply their knowledge for implanting problem solving approach and design methodology and enhance the modern usage.
2. To inculcate subjects that focuses on skill development to enhance employability of the students.
3. Modify the scheme/syllabus as to provide a bridge among fast and slow leaners



SHRI G. S. INSTITUTE OF TECHNOLOGY AND SCIENCE
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION
MINOR DEGREE IN ELECTRONICS ENGINEERING (VLSI DESIGN) Tentative

Semester : To be done by Students between III to VI semester

Pre-requisite : Basic Electronics & Digital Electronics

Sr No	Course Code	NPTEL COURSE	L	T	P	Credits
1	VLSI - 01 (12 Weeks)	Microelectronics : Devices & Circuits OR Introduction to Semiconductor Devices OR Semiconductor Devices & Circuits	3	0	0	3
2	VLSI - 02 (8 Weeks)	CMOS Digital VLSI Design	2	0	0	2
3	VLSI - 03 (8 Weeks)	System Design through Verilog	2	0	0	2
4	VLSI - 04 (12 Weeks)	Digital IC Design OR Design & Analysis of VLSI subsystems	3	0	0	3
5	VLSI - 05 (12 Weeks)	Analog IC Design OR Analog VLSI Design	3	0	0	3
6	VLSI - 06 (12 Weeks)	C Based VLSI Design	3	0	0	3
7	VLSI-07 (12 Weeks)	Low Voltage CMOS Circuit Operations OR VLSI Interconnects	2	0	0	2
8	VLSI-08 (12 Weeks)	Digital VLSI Testing	3	0	0	3
		Total				21

7/01/25
Vaibhav Neema