

Shri G. S. Institute of Technology & Science, Indore
Department of Electronics & Instrumentation Engg.

Session July-Dec 2025

Mid-Sem Test -3 (Optional) Time Table

Date	Time	II Year	III Year	IV Year
6/11/2025	10-11AM	EI27011 Circuit Analysis & Synthesis	EI37006 Microprocessor Systems	EI47053 Process Instrumentation
	12-1PM	EI27012 Fundamentals of Measurement	IT37005 Data Structure	BM47xxx Medical Instrumentation/ VLSI Technology (BA group)
7/11/2025	10-11AM	EI27013 Electronic Devices & Circuits	EI 37007 CMOS VLSI Design / EE37003 Control System (BA group)	EI47322 VLSI Technology EE47002 Power Electronics
	12-1PM	EI27016 Digital Electronics	EC37254/EI37255/37xxx Analog & Digital Communication/ Smart Sensors/P & I	EI47257 Fiber Optics & Photonics
	2:30-3:30PM	MA27014 Mathematics-III	IP37251 Industrial Engg. & Mgt./CO37253 Artificial Intelligence	EI47xxx Automation in Instru./IoT EI47322 VLSI Design (BA group)
	3:30-4:00PM		HUM3702 Constitution of India	

On 3/NOV - 4/NOV, there would be regular classes (as per time-table)

Note: After 15 minutes of commencement of test no student is allowed to appear in test.

Copy to:

1. Head, Applied Mathematics Deptt.
2. Head, Electrical Engg. Deptt.
3. Head, Humanities. Deptt.
4. Head, IT Deptt.
5. Head, IP Deptt.
6. Head, E & Tc dept.
7. Head BM, dept.


29/10/25

HOD-EI

Dr. Rajesh Khatri