

SHRI G S INSTITUTE OF TECHNOLOGY & SCIENCE, INDORE
I M.E. ELECTRONICS AND COMMUNICATIONS ENGINEERING

SEMESTER - I

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EC65001	Information Theory and Coding	4	-	-	4	-	70	30	-	-	100
2	EC65002	Stochastic Process & Queuing Systems	4	-	-	4	-	70	30	-	-	100
3	EC65003	Network Design Technology	4	-	-	4	-	70	30	-	-	100
4	ELECTIVE I		4	-	-	4	-	70	30	-	-	100
5	ELECTIVE II		4	-	-	4	-	70	30	-	-	100
6	EC65451	Lab.- I	-	-	4	-	4	-	-	40	60	100
7	EC65452	Lab.- II	-	-	4	-	4	-	-	40	60	100
8	EC65500	Comprehensive Viva	-	-	-	-	-	-	-	-	Grade	Grade
Total			20	0	8	20	8	350	150	80	120	700

SEMESTER - II

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EC65501	Cryptography & E-Security	4	-	-	4	-	70	30	-	-	100
2	EC65502	Mobile & Satellite Communication	4	-	-	4	-	70	30	-	-	100
3	EC65503	Optical Communication	4	-	-	4	-	70	30	-	-	100
4	ELECTIVE III		4	-	-	4	-	70	30	-	-	100
5	ELECTIVE IV		4	-	-	4	-	70	30	-	-	100
6	EC65851	Lab.- III	-	-	4	-	4	-	-	40	60	100
7	EC65852	Lab.- IV	-	-	4	-	4	-	-	40	60	100
8	EC65881	Seminar	-	-	2	-	2	-	-	100	-	100
9	EC65900	Comprehensive Viva	-	-	-	-	-	-	-	-	Grade	Grade
Total			20	0	10	20	10	350	150	180	120	800

SHRI G S INSTITUTE OF TECHNOLOGY & SCIENCE, INDORE
I M.E. ELECTRONICS AND COMMUNICATIONS ENGINEERING

List of Electives

Elective I		Semester I	Elective II		Semester I
S.No	Sub. Code	Subject	S.No	Sub. Code	Subject
1		System Integration and Databases	1		Advanced Digital Communication
2		Telecommunication Business Engineering	2		Network Management Protocols
3	CO65203	Programming System	3		Embedded System's programming
4		Advanced Engineering Mathematics	4		Distributed Computing System
			5	EC65305	Data Communication(EC 6535)

Elective III		Semester II	Elective IV		Semester II
S.No	Sub. Code	Subject	S.No	Sub. Code	Subject
1		Modelling & Simulation of Computer Networks	1		Frequency Regulations and their Management
2		Digital Signal Processing	2	EC65751	Unix Network Programming (EC 6582)
3		Operating Systems	3		Real Time Operating Systems
4		Software Project Management	4		Software Engineering
5		VLSI & ASIC Design	5		Embedded System Design
6	EC65701	Digital Signal Processing	6	EC65752	Microwave Measurements & Design
			7		Design of Intrigrated Circuits

SHRI G S INSTITUTE OF TECHNOLOGY & SCIENCE, INDORE
II M.E. ELECTRONICS AND COMMUNICATIONS ENGINEERING

SEMESTER - III

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EC65901	Dissertation Phase-I	-	-	20	-	20	-	-	100	150	250
Total			0	0	20	0	20	0	0	100	150	250

SEMESTER - IV

(Admitted before 2012)

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EC 6596	Dissertation Phase-II	-	-	22	-	22	-	-	160	240	400
Total			0	0	22	0	22	0	0	160	240	400

(Admitted in 2012-2016)

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EC 6594	Dissertation Phase-II	-	-	30	-	30	-	-	160	240	400
Total			0	0	30	0	30	0	0	160	240	400

(Admitted in and after 2017)

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EC65951	Dissertation Phase-II	-	-	30	-	30	-	-	160	240	400
Total			0	0	30	0	30	0	0	160	240	400