Shri G. S. Institute of Technology and Science, Indore

Department of Electronics and Instrumentation Engineering

Requirement of Project Associate Under C2S Project Funded by MeitY, Govt of India

Project: Chip-to-Startup (C2S)

Funding: Ministry of Electronics and Information Technology (MeitY)

No. of Posts: 02

Duration: Initial appointment will be for one year, extendable upto one year subjected to satisfactory performance.

Essential Qualification: B. Tech., M. Tech. in VLSI Design with Proficiency in using EDA tools Cadence, Siemens, Synopsys, Xilinx, Vivado, FPGA

Stipend/Salary: Rs.40600/- per month Consolidated including HRA

Job Description: Carry out ongoing project on Pacemaker Chip, Coordinate with cluster institute, Maintain EDA tools & workstations in Labs, Prepare documentation.

How to Apply: Eligible and interested candidates may apply using Google link: https://forms.gle/bpFspXYVWX5KTgNy7

**For more information-
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