

## Curriculum Vitae

<b>1. Personal Details</b>		
(i)	Name	Khushboo Nagar
(ii)	Father's Name	Mr. Dileep Kumar Nagar
(iii)	Date of Birth	16/05/1994
(iv)	Address for Communication	Assistant Professor Department of Electrical Engineering. SGSITS, Indore 23, Park Road, Indore (M. P.) - 452003
(v)	Permanent Address	44, Shikshak Nagar Ext., Airport Road Indore (MP)
(vi)	Contact Details	+91 8817883446/ +91 9174811244
(viii)	Nationality/Religion	Indian



<b>2. Educational Qualifications</b>					
S. No.	Degree	Year	University/Institute	Pointer	Specialization
1	M.Tech.	July2015- Dec2017	Vindhya Institute of Technology and Science, Indore	8.29	Power Electronics
2	B.E.	July2011 - May2015	Vindhya Institute of Technology and Science, Indore	8.00	Electrical and Electronics Engineering

**M.Tech. Dissertation Title:** “A Comparative Study on Condition Monitoring of HVDC Transmission System, using Different Artificial Neural Network Techniques”

<b>3. Career History and Profile of Work Experience (06 Years, 04 Months)</b>	
<b>Present Position Held:</b> Assistant Professor (Contract Faculty)	
<b>(a) Academic / Teaching Experience and subjects taught</b>	
1	Assistant Professor, Department of Electrical Engineering, SGSITS, Indore, Oct 2018 – Feb 2022. Assistant Professor, Department of Electrical & Electronics Engineering, VITS, Indore, Dec 2017- Sept 2018. Teaching Assistant, Department of Electrical & Electronics Engineering, VITS, Indore, July 2015-Dec 2017.
2	<b>UG Subjects Taught (Theory):</b> i) Network Analysis ii) Fundamentals of Electrical Engineering iii) High Voltage Direct Current & FACTS iv) Energy and Environment Audit and Modeling v) Electrical and Electronics Measurement and Instrumentation vi) Electrical Drives vii) Basic Electrical Drives and System

3	<b>UG Subjects (Laboratory):</b>			
	i) Network Analysis			
	ii) Fundamentals of Electrical Engineering			
	iii) High Voltage Direct Current & FACTS			
	viii) Energy and Environment Audit and Modeling			
	iv) Electrical Workshop-II (Projects)			
	v) Electrical Machine-I			
<b>(b)</b>	<b>Other Academic Experiences and positions/responsibilities held</b>			
	<b>Name of Post Held</b>	<b>Name of the Institution</b>	<b>Period</b>	
			<b>From</b>	<b>To</b>
1.	Time Table Coordinator	VITS, Indore	July 2015	July 2018
2.	MST Exam Coordinator	VITS, Indore	July 2015	July 2018

<b>(c)</b>	<b><u>Area of research interest:</u></b>
	Power Electronics, HVDC fault detections using Soft Computing Methods

<b>(d)</b>	<b><u>Technical Papers Publications</u></b>
	<b><u>International and National Journals</u></b>
1.	“A Review on Different ANN Based Fault Detection Techniques for HVDC Systems”, <i>International Journal of Innovative Research in Engineering &amp; Management (IJIREM)</i> , Volume-3, Issue-6, November-2016, pp.477-481.
2.	“A Comparative Study on Condition Monitoring of HVDC Transmission System using Artificial Neural Network Techniques”, <i>Journal of Power Electronics and Devices (MAT Journal)</i> , Volume 5, Issue 3, year 2019.

<b>4.</b>	<b><u>Academic cum Administrative Experiences</u></b>
1.	One day visit at NHPC limited, (Omkareshwar Project), Khandwa District of Madhya Pradesh (2019).
2.	Visited a 400kV transmission station MPPTCL, Khandwa Road, Indore, MP (2016)

<b>5.</b>	<b>Academic Contributions</b>
<b>(a)</b>	<b>Experiments added</b>
1.	<b>Fundamentals of Electrical Engineering-</b> Introduced a new experiment in lab manual about “Measurement of resistance using color coding method and verify by using multimeter”. Also about “Earthing” in first experiment, Introduction to machine laboratory.
<b>(b)</b>	<b>Hardware Projects Undertaken</b>
1.	Supervised batches of 2 <sup>nd</sup> year students of electrical engineering branch for hardware project implementation under the course <b>Electrical Workshop –II</b> (2019-2020).

(c)	<b>Journal Update</b>
1.	Fundamentals of Electrical Engineering (2019 (A))

<b>6.</b>	<b><u>List of Seminars / Workshops/Summer/Winter Courses Attended</u></b>		
<b>(a)</b>	<b>Workshops and Courses and FDP</b>		
	<b>Workshops/ Seminar</b>	<b>Duration</b>	<b>Organized by</b>
1.	Gandhi Global Solar Yatra	1 Day (2019)	IIT Bombay
2.	Examination Reforms for Teachers	1 Day (2019)	AICTE
3.	Assessment of Online Courses for TEQIP Institutes	1-1 Days (2020)	TEQIP Phase-III
4.	Being a Great Teacher	1 Day (2015)	IBS Business School, at VITS, Indore (M.P.)
5.	MATLAB	1 Day(2014)	VITS, Indore(MP)
<b>(b)</b>	<b>Courses Attended and FDP</b>	<b>Duration</b>	<b>Organized by</b>
1.	Network Analysis	Jan-April 2020	IIT Kharagpur
2.	Basic Electric Circuit	July-Oct 2019	IIT Kanpur (NPTEL Online Certification)
3.	Fundamentals of Electrical Engineering (FDP)	5 Days (2019)	TEQIP-III
4.	Robotics and AI	5 Days (2019)	Medicaps University collaboration with E & ICT Academy IITDM Jabalpur
5.	Electromagnetic Field Theory (FDP)	5 Days (2018)	TEQIP
6.	Advancement in Energy Audit & Energy Management	2 Days	SAIT, Indore, under TEQIP-III
<b>(c)</b>	<b>Webinars</b>	<b>Duration</b>	<b>Organized by</b>
1.	Research Opportunities in Power Electronics	1 Day (2020)	EED, SGSITS, Indore
2.	High Performance Computing	1 Day (2020)	EED, SGSITS, Indore
3.	Enhancement of Testing and Consultancy in the Institute	1 Day (2020)	EED, SGSITS, Indore
4.	Machine Learning	1 Day (2020)	IT Department, SGSITS, Indore
5.	Exponential Technology in Education	1 Day (2020)	IT Department, SGSITS, Indore
6.	Scope of Electrical Engineering in EPC Industry ( Engineering, Procurement & Construction)	1 Day (2020)	EED, Prestige Institute Of Engineering Management & Research, Indore
7.	Electricity Market And Use of Block chain In Power Sectors	1 Day (2020)	EED, Prestige Institute Of Engineering Management & Research, Indore
8.	Energy Conservation in Irrigation System	1 Day (2020)	EEED, Angadi Institute Of Technology & Management Savagoan Road,

			Belagavi, Karnataka
9.	Gender Equality and Women's Rights	1 Day (2020)	National Institute of Technology, Kurukshetra Under TEQIP-III
10.	Awake, Encourage and flourish Feminism	3 Days (2020)	CSE Department, GCE, Aurangabad Under TEQIP-III
11.	Mixed Signal Design- Testing and challenges	1 Day (2020)	E & TC Department, SGSITS, Indore
12.	Introduction to Optimization and Jaya Optimisation Technique	1 Day (2020)	EEED, LNCT, Indore

**( KHUSHBOO NAGAR )**