


Prof./Dr./Mr./Ms.: RAMCHANDRA GURJAR

1. Personal Information			
(i)	Name	RAMCHANDRA GURJAR	
(ii)	Qualification	M.E., Ph.D. Pursuing	
(iii)	Designation	Assistant Professor	
(iv)	Email-id	rcgurjar94@gmail.com,rgurjar@sgsits.ac.in	
(v)	Employee No.	3300307	
(vi)	Department	Dept. Of Electronics & Instru. Engg	
(vii)	Experience	13Yrs	

2. Educational Qualification				
S. No.	Degree	Specialization	Year	University/Board
01	Ph.D. Pursuing	Electronics & Instrumentation	2012	RGPV, Bhopal
02	M.E.	Digital Technique & Instrumentation	2006	RGPV, Bhopal
03	B.E.	Electronics & Comm. Engg	2001	RGPV, Bhopal

3. Research Interests
Analog RFIC Design, Design of Microprocessors and Microcontrollers

4. Research Paper Publications	
(I) International/National Journal Publications	
(I)	R.C.Gurjar and D.k.Mishra "A Wide Tuning Range Self-Switched Biasing VCO in 0.18 μ m CMOS Technology" in international journal of information technology and electrical engineering, vol-8 issue-5, ISSN: - 2306-708X oct-2019.
(II)	Devisingh Baghel and R.C.Gurjar "A Low Noise, Voltage Controlled Ring Oscillator based on pass transistor delay cell" in International journal of Innovative Research in Computer and

- Communication Engineering, Vol-04, issue-07, July 2016. ISSN(Online):2310-9801ISSN(Print):2320-9798, doi:10.15680/IJIRCCE.2016.0407001
- (III) Ranjana Mehra and R.C.Gurjar "LC VCO using Active inductor for Low Phase Noise and wide Tuning Range" in International journal of Innovative Research in Science, Engineering and Technology" Vol-5, issue-7, July 2016.ISSN(online):2319-8753,ISSN(Print):2347-6710, doi:10.15680/IJRSET.2016.0507106
- (IV) Deepika and R.C.Gurjar "Low Phase Noise, Current Starved Ring VCO with Wide Tuning Range and Improved Linearity" in International journal of Innovative Research in Science, Engineering and Technology" Vol-5, issue-7, July 2016.ISSN(online):2320-9801,ISSN(Print):2320-9798, doi:10.15680/IJIRCCE.2016.0407078
- (V) Pankaj Aseri and R.C.Gurjar "A low Power , Low Phase Noise CMOS LC Oscillator" in International Journal of Electrical and Electronics Engg. Vol-2, spl. Issue, E-ISSN:1694-2310, P-ISSN : 1694-2426 during July 2015 at NITTTR Chandigarh
- (VI) Rohit Mongre and R.C.Gurjar "Design of Low Power& High Speed Comarator with 0.18 um Technology for ADC Application in Int journal of Engineering and Applications ISSN:2248-9622 Vol-4 issue-8, Aug-2014. Pp 146-153
- (VII) Manoj K Pandram,R.C.Gurjar, "A Low Power down Conversion CMOS Gilbert Mixer for Wireless Communicaitons, In Interantion journal of Engineering ,Research and Applications, ISSN :2248-9622, Vol.4,Issue7(Version1) July 2014pp 186-190.
- (VII) R.C.Gurjar,Rupali Jarwal and Ulka Khire " A Low Power Digitally Controlled Oscillator using .18 um technology" in international journal of engineering research & technology, ISSN: 2278-0181,vol:2 issue 4 Apr-2013.
- (VIII) Jayendra Sikarwar and R.C.Gurjar " A Low cost first order sigma delta convertor design and analysis using .18 um technology" international journal of engineering research and applications, ISSN 2248-9622,vol 2 issue 1 Jan-12 pp.668-671.

(II) International/National Conference Publications

- (I) Shailesh Jakodiya, R.C.Gurjar and R.S.Gamad" Design of OTA Based Band Pass Filter using 180nm Technology" in 5th international conference on Microelectronics, Computing& Communication System (MCS-2020) 11-12th July 2020 at ARTTC BSNL Ranchi organized by Institution of Electronics & Telecommunication Engineer & Indian Society for VLSI Design, Ranchi.
- (II) Raviraj Singh,Rajesh Khatri and R C Gurjar" Design of multifuncational small digital integrated circuits(IC) using 180nm CMOS" in Fourth International Conference on Electronics, Communication and Aerospace Technology (ICECA-2020) IEEE Xplore Part Number: CFP20J88-ART; ISBN: 978-1-7281-6387-1
- (III) Harsheeta Kushwah,R S Gamad and R C Gurjar" Design of Low Power & High Speed Comparator of SAR ADC using 180nm Technology" in Fourth International Conference on Electronics, Communication and Aerospace Technology (ICECA-2020) IEEE Xplore Part Number: CFP20J88-ART; ISBN: 978-1-7281-6387-1
- (IV) R.C.Gurjar and D.K.Mishra " Design and Performance Analysis of Active Inductor with Low Voltage and High Q Active Inductor" in 5th IEEE International Conference on Inventive Computation Technologies(ICICT-2020), during 26-28 Feb 2020.DOI: [10.1109/ICICT48043.2020.9112505](https://doi.org/10.1109/ICICT48043.2020.9112505) **Electronic ISBN:** 978-1-7281-4685-0 ,**Print on Demand(PoD) ISBN:** 978-1-7281-4686-7 , Coimbatore (India)/26-28th Feb-2020,RVS Technical Campus, Coimbatore
- (V) BOOK Chapter (Springer Nature Singapore Pte Ltd.2019) Shivendra Singh Parihar and Ramchandra Gurjar "Compact Modelling of Drain Extended MOS Transistor using BSIM-BULK Model" in VLSI Desgin and Test (VDAT)-2019 during July 04-06,2019 at IIT Indore.ISSN 1865-0929 ISSN 1865-0937 (electronic) **Communications in Computer and Information Science** ISBN 978-981-32-9766-1 ISBN 978-981-32-9767-8 (eBook) <https://doi.org/10.1007/978-981-32-9767-8>
- (VI) Ashu Tilwe, R.C.Gurjar, D.K.Mishra "Low Phase Noise Current Starved Ring VCO on MOS Capacitance" in International journal of innovative technology and exploring engineering (IJITEE)

vol-8 issue-10, august-2019, ISSN:22783075

- (VII) Shreyas Tiwari, Rajesh khatri and Ram Chandra Gurjar "RF Double Balanced Current Mirror UP-Conversion Mixer at 2.4 Ghz frequency" in IEEE Sponsored IEMENTech 2019 at IEM Kolkata during august 2019. DOI: [10.1109/IEMENTech48150.2019.8981073](https://doi.org/10.1109/IEMENTech48150.2019.8981073) Electronic ISBN: 978-1-7281-5543-2 ,Print on Demand(PoD) ISBN: 978-1-7281-5544-9
- (VIII) Pradeep Rajpoot, R.C.Gurjar, R.S.Gamad, "Low Phase Noise Wide Tuning Range LC Oscillator for RF Application using Varactor Bank" in Journal of Telecommunicaiton,Electronic and Computer Engineering(JTEC) , Volume-11 no-3 July-sep-2019 ISSN:2180-1843 E-issn:2289-8131.
- (IX) Jitendra Patidar, R.Khatri, R.C.Gurjar "Precision Agriculture System Using Verilog Hardware Description Language to desing an ASIC" in IEEE Sponsored IEMENTech 2019 at IEM Kolkata during august 2019. DOI: [10.1109/IEMENTech48150.2019.8981128](https://doi.org/10.1109/IEMENTech48150.2019.8981128) Electronic ISBN: 978-1-7281-5543-2 Print on Demand(PoD) ISBN: 978-1-7281-5544-9
- (X) khyati peepra and R.C.Gurjar" A Linear Current Starved Voltage Controlled Ring Oscillator with wide Tuning Rang using 180nm CMOS Technology" in [2018 International Conference on Recent Innovations in Electrical, Electronics & Communication Engineering \(ICRIEECE\)](https://doi.org/10.1109/ICRIEECE44171.2018.9008640), during 27-28 July 2018. DOI: [10.1109/ICRIEECE44171.2018.9008640](https://doi.org/10.1109/ICRIEECE44171.2018.9008640) School of Electrical Engineering,Kalinga Institute of Industrial Technology (KIIT)
- (XI) Pradeep Solanki , R.C.Gurjar and D.K.Mishra "Design of LC-tank Digitally controlled Oscillator with variable capacitor" 4th International conference for convergence in technology during 27-28 Oct-2018 at Manglore India. [2018 IEEE, 4th International Conference for Convergence in Technology \(I2CT\)](https://doi.org/10.1109/I2CT42659.2018.9058250) DOI: [10.1109/I2CT42659.2018.9058250](https://doi.org/10.1109/I2CT42659.2018.9058250) SDMIT Ujire Manglore
- (XII) Shivendra s parihar, Chandan jha, Ramendra Singh, Ramchandra Gurjar and Abhishek Dixit "Characterization and Global Parameter Extraction of bulk MOSFETS using BSIM-BULK Model" in 5th IEEE UP section International Conference on Electrical, Electronics and Computer Engineering(UPCON) during 2-4 Nov-2018, Madan Mohan Malviya University of Technology,Gorakhpur DOI: [10.1109/UPCON.2018.8596970](https://doi.org/10.1109/UPCON.2018.8596970)
- (XIII) Narendra Singh Dhakad and R C Gurjar "A Low Phase Noise VCO using Injection Locking In IEEE, International Conference on information, communication, instrumentation and control (ICICIC-2017) during 17-19 august 2017 held at Medicaps university Indore. DOI: [10.1109/ICOMICON.2017.8279101](https://doi.org/10.1109/ICOMICON.2017.8279101) Electronic ISBN: 978-1-5090-6313-0 Print on Demand(PoD) ISBN: 978-1-5090-6314-7
- (XIV) Shephali singh and R C Gurjar "A Low power, Low phase noise VCO using cascaded active inductor In IEEE, International Conference on information, communication, instrumentation and control (ICICIC-2017) during 17-19 august 2017 held at Medicaps university Indore. DOI: [10.1109/ICOMICON.2017.8279097](https://doi.org/10.1109/ICOMICON.2017.8279097) Electronic ISBN: 978-1-5090-6313-0 Print on Demand(PoD) ISBN: 978-1-5090-6314-7
- (XV) Shephali singh and R C Gurjar "A Low Power, Low phase noise wide tuning LC VCO with Varactor Bank" in International conference on Recent Innovations in Signal processing and Embedded Systems(RISE) during 27-29 Oct 2017 held at MANIT Bhopal pages 413-417. DOI: [10.1109/RISE.2017.8378191](https://doi.org/10.1109/RISE.2017.8378191) Electronic ISBN: 978-1-5090-4760-4 Print on Demand(PoD) ISBN: 978-1-5090-4761-1
- (XVI) Narendra Singh Dhakad and R C Gurjar "A Wide tuning range voltage controlled ring oscillator using injection locking" in International conference on Recent Innovations in Signal processing and Embedded Systems(RISE) during 27-29 Oct 2017 held at MANIT Bhopal pages 418-421 DOI: [10.1109/RISE.2017.8378192](https://doi.org/10.1109/RISE.2017.8378192) Electronic ISBN: 978-1-5090-4760-4 Print on Demand(PoD) ISBN: 978-1-5090-4761-1
- (XVII) D S Baghel and R.C.Gurjar : Design of wide tuning range VCRO based on single ended delay cell using CMOS 0.18 um technology" in IET Sponsored International conference on signal processing at SATI Vidisha during 7-9 Nov-2016. ISBN: 978-1-78561-783-6 DOI: [10.1049/cp.2016.1449](https://doi.org/10.1049/cp.2016.1449)
- (XVIII) Jyoti Dangi and R.C.Gurjar " An ECG Instrumentation amplifier with improved CMRR and Gain using 0.18 um Technology" in proceedings of 11th IRF international conference ,15th june -2014, pune india ISBN:978-93-84209-27-8
- (XIX) Jyoti Dangi and R.C.Gurjar "Low Voltage two stage CMOS op-amp for biomedical application using

0.18 um Technology” in international conference in computer science ,Electronics and Communication Engineering- ICCECE,2014 ISBN: 978-3-643-24819-10, Bangalore,27th july-2014.

5. List of Conferences/Workshops/Seminars Organized

- (I) Organized Teqip sponsored 2 week workshop on “Verilog and Basic system verilog Assertions” in association with TECHLABS Hyderabad during Feb1-12,2014.
- (II) Organized Teqip sponsored 1 week workshop on “Integrated circuit Design with mentor graphic EDA tools from specification to GDSII” in association with TECHLABS Hyderabad during 15-19 dec-2014.
- (III) Contributed as Teaching Assistant in Two Week ISTE STTP Workshop on “CMOS ,Mixed Signal and Radio frequency VLSI Design” conducted by IIT Kharagpur from 30th Jan to 04th Feb 2017 at SGSITS Indore.
- (IV) Contributed as an Organizing Committee Member for the short term course on “Analog and Mixed Signal System Design” held at SGSITS Indore during 23-27 March 2015
- (V) Contributed as an Organizing Committee Member for a workshop conducted by SGSITS on “Janjatiya Jivansheli aur Bhartiya Jeevan Drashti” held at SGSITS indore on 30/07/2017.
- (VI) Contributed as an Organizing Committee Member for the central zone workshop conducted by Vibha Vani (VIBHA Association of NGOs India) on “Capacity building of NGOs for Nation building” held on 16-17th Sep.2017 at SGSITS Indore.
- (VII) Organized TEQIP-III Sponsored one week STC on “Recent Trends in VLSI Design” as a Co-coordinator during 09-13 April 2018 at SGSITS Indore.
- (VIII) Contributed as an Organizing Committee Member for the short term course on “Advancements in Microelectronics and VLSI Design” during 11th -15th March 2019 Conducted by Dept of Electronics & Instrumentation Engineering, SGSITS Indore.
- (IX) Contributed as an Organizing Committee Member for the AICTE Approved short term training programme on “Design and Development of System on Chip using Low Power VLSI” during 25th -30th Nov 2019 Conducted by Dept of Electronics & Instrumentation Engineering, SGSITS Indore.
- (X)Organized one week online AICTE Approved STTP on “ Mixed Signal and Radio frequency VLSI Design” as a co-coordinator during 23-28/11/2020

List of Webinar Organized:

- (I) Coordinated a webinar on” Hardware Accelerator for Machine Learning Application”** Conducted by Electronics & Instrumentation dept,SGSITS on 01/06/2019.
- (II)Co-coordinated an international webinar on” Technology Trends in Automotive Electronics-An Overview”** :Hardware Accelerator for Machine Learning Application” Conducted by Electronics & Instrumentation dept,SGSITS on 20/06/2019.

6. Invited Lectures/Expert Talks/Chairmanships at Conferences

(I). Delivered Expert Lecture in a MPCOST sponsored Faculty Development Program on “CAD Based Learning Methodologies for Electronic Design Automation “on 12 March 2018 at LNCTS Bhopal

(II). Expert Lecture delivered on “Power Electronics” in Govt Polytechnic Shajpur on 09/11/2016.

(III). Delivered an expert lecture on “Air Pollution and spreading awareness on poor Air Quality” at SGSITS Indore on 2nd June 2019.

(IV). Performed as session chair in the IEEE international conference on Data, Engineering and Applications at RGPV Bhopal during 28-29/02/2020.