


Prof./Dr./Mr./Ms: RAJESH KHATRI

1. Personal Information			
(i)	Name	RAJESH KHATRI	Photo 
(ii)	Qualification	BE(Electronics) ME(Instrumentation)	
(iii)	Designation	Associate Professor	
(iv)	Email-id	rajeshkhatri1@rediffmail.com	
(v)	Employee No.	3300279	
(vi)	Department	Electronics and Instrumentation	
(vii)	Experience	27 years	

2. Educational Qualification				
S. No.	Degree	Specialization	Year	University/Board
1.	B. E.	Electronics Engg.	1990	Amravati University
2.	M. E.	Instrumentation	1995	Aurangabad University

3. Research Interests
VLSI Design, RFIC Design and High Frequency Engineering

4. Research Paper Publications			
(I) International/National Journal Publications			
Sr. No	Title of Paper with Page No.	Journal Name	ISSN/ISBN No.
1	High Swing Ultra-Wide Bandwidth with Very High CMRR Fully Differential Operational Amplifier in 0.18um technology pp. 58-63	International Journal of Innovations in Engineering and Technology, Special issue 2013	ISSN 2319-1058
2	Design of Hysteretic CMOS Comparator for Audio Amplifier using 180nm technology pp. 452-458	International Journal of VLSI and Embedded Systems, Vol.4, June 2013	ISSN 2249-6556
3	An Ultra Low Power Low Voltage Fully	International Journal of	ISSN

	Differential Op-amp for Long Life Autonomous Portable Equipment pp 81-85	Engineering Research and Development, Vol.7 Issue 1, May 2013	2278-067X
4	Design of High Speed Low Power Multiplier Using Nikhila Sutra with Help of Reversible Logic pp 159 - 167	International Journal of Modern Engineering Research Vol.4, Issue 4, Jan-2014	ISSN 2249-6645
5	A 2.4GHZ Differential Low Noise Amplifier Using 0.18um CMOS Technology pp 207-212	International Journal of Electronic and Electrical Engineering Vol.7, No. 3, 2014	ISSN 0974-2174
6	Design of High Speed Low Power Reversible Logic Adder Using HNG Gate pp 152-159	International Journal of Engineering Research and Applications Vol.4, Issue 1, Jan. 2014	ISSN 2248-9962
7	A Fully Optimized Ultra Wide Band Mixer in 65nm Technology pp 1 - 7	International Journal of Electronics IARAS Publication	ISSN 2367-8879
8	Design of BIST Architecture of 8x8 SRAM Testing Using Transient Current Method pp 5931-5040	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering Vol.4 issue 7 July-2015	ISSN 2320-3765
9	8KB Logic Compatible DRAM Based Memory Design for Low Power Systems pp 1267-1271	International Journal of Science and Research Vol4 issue 8 Aug.2015	ISSN 2319-7064
10	Design of Low Power, High Gain PLL using CS-VCO in 180nm Technology pp 26 - 31	International Journal of Computer Applications Vol.122, No 18 July-2015	ISSN 0975-8887
11	Function Generator Using Current Conveyers CCII pp 1 - 4	International Journal of Computer Applications Vol. 147, No 7 Aug-2016	ISSN 0975-8887

(II) International/National Conference Publications

S. No.	Title of paper Presented	Title of the Conference/ Seminar	Organized by
1.	Phase Rotated Digital Fractional Divider for RF SOC's	8 th International conference on Computing, Communication and Networking Technologies	IEEE and IIT Delhi
2.	A 0.45V, 1.3nW Band gap CMOS Voltage Reference with Temperature Compensation.	4 th IEEE Uttar Pradesh Section International Conference on Electrical, computer and Electronics (UPCON)	IEEE and GLA University Mathura
3.	Design of 2.4GHz Differential Low Noise Amplifier using 180nm Technology	IEEE International conference on Recent Innovations in Signal Processing and Embedded Systems(RISE)	IEEE and MANIT Bhopal

4	RF Double Balanced Current Mirror UP-Conversion Mixer at 2.4 Ghz frequency	IEEE Sponsored IEMENTech 2019	IEM Kolkata august 2019.

5. List of Conferences/Workshops/Seminars Organized

1. Organized **two** week training programme on “ Verilog and System Verilog Assertions” during 1st to 12th Feb. 2014 at SGSITS Indore in association with TECHLABS Hyderabad.
2. Organized **one** week TEQIP-II sponsored training programme on “Integrated Circuit Design Using Mentor Graphics EDA Tools: From Specifications to GDSII” during 15th to 19th December 2014 at SGSITS Indore in association with TECHLABS Hyderabad.
3. International Webinar on "Technology Trends in Automotive Electronics - An Overview" was delivered by Dr. Sanjay S. Mishra, Autonomous car- Program Manager, Ford Motors, MI, USA and coordinated by Mr. Rajesh Khatri, Associate Professor on 20th June 2020.

6. Invited Lectures/Expert Talks/Chairmanships at Conferences

1. Delivered expert lectures on “Layout Design using CADENCE tool” on 3rd March, 4th March and 5th March, 2017 at IET DAVV Indore
2. Delivered an online expert lecture on "EDA tools for VLSI Applications" at IEEE sponsored webinar at Acropolis Institute of Technology and Research, Indore on 29th June 2020.