

CURRICULUM VITAE

1. Personal Information			
(i)	Name	Ms. Ritika Nair	
(ii)	Qualification	M.E	
(iii)	Designation	Assistant professor	
(iv)	Email-id	ritika.v.nair@gmail.com	
(v)	Employee No.	405015	
(vi)	Department	Electronics and Telecommunication	
(vii)	Experience	8 years	

2. Educational Qualification				
S. No.	Degree	Specialization	Year	University/Board
1	M.E	VLSI DESIGN	2016	RGPV
2	B.E	EC	2009	RGPV

3. Research Interests
Digital Electronics

4. Research Paper Publications
(I) International/National Journal Publications
Ritika, Vijay Sharma, Megha Soni, Design of Ring VCO based PLL using 0.25 μm CMOS Technology, IJIRT, Volume 2 Issue 9, ISSN 2349-6002, February 2016.

Ms.Ritika Nair
Assistant Professor, E&TC