

**I M. Tech. (Microelectronics & VLSI Design)****Semester-I**

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	<b>EI69002</b>	Design of Integrated Circuits	4	-	-	4	-	70	30	-	-	100
2	<b>EI69003</b>	Microelectronics	4	-	-	4	-	70	30	-	-	100
3	<b>EI69004</b>	VLSI Technology	4	-	-	4	-	70	30	-	-	100
4	<b>EI69005</b>	Semiconductor Device and Process Modeling	4	-	-	4	-	70	30	-	-	100
5	Elective I		4	-	-	4	-	70	30	-	-	100
6	<b>EI69451</b>	Lab.- I : CAD Tool Design 1	-	-	4	-	4	-	-	40	60	100
7	<b>EI69452</b>	Lab.- II : CAD Tool Design 2	-	-	4	-	4	-	-	40	60	100
8	<b>EI69500</b>	Comprehensive Viva	-	-	-	-	-	-	-	-	Grade	Grade
<b>Total</b>			<b>20</b>	<b>0</b>	<b>8</b>	<b>20</b>	<b>8</b>	<b>350</b>	<b>150</b>	<b>80</b>	<b>120</b>	<b>700</b>

**Semester-II**

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	<b>EI69501</b>	Communication RF IC Design	4	-	-	4	-	70	30	-	-	100
2	<b>EI69502</b>	Mixed Signal Circuit Design	4	-	-	4	-	70	30	-	-	100
3	<b>EI69503</b>	VLSI Test and Testability	4	-	-	4	-	70	30	-	-	100
4	<b>EE69505</b>	Digital Signal Processing	4	-	-	4	-	70	30	-	-	100
5	Elective II		4	-	-	4	-	70	30	-	-	100
6	<b>EI69851</b>	Lab.- III : VLSI Design Lab.1	-	-	4	-	4	-	-	40	60	100
7	<b>EI69852</b>	Lab.- IV : VLSI Design Lab.2	-	-	4	-	4	-	-	40	60	100
8	<b>EI69881</b>	Seminar	-	-	2	-	2	-	-	100	-	100
9	<b>EI69900</b>	Comprehensive Viva	-	-	-	-	-	-	-	-	Grade	Grade
<b>Total</b>			<b>20</b>	<b>0</b>	<b>10</b>	<b>20</b>	<b>10</b>	<b>350</b>	<b>150</b>	<b>180</b>	<b>120</b>	<b>800</b>

**I M.Tech (Microelectronics & VLSI Design)****List of Elective**

Elective I			Semester I			Elective II			Semester II		
S.No	Sub. Code	Subject	S.No		Sub. Code	Subject					
1	<b>EI69201</b>	Application Specific Integrated Circuits	1		<b>EI69701</b>	Image Processing					
2	<b>EI69202</b>	High Power Semiconductor Devices	2		<b>EI69702</b>	Microwave Integrated Circuits					
3	<b>EI69203</b>	System Hardware Design	3		<b>EI69703</b>	Optoelectronic Integrated Circuits					
4	<b>EI69204</b>	Hardware Description Language	4		<b>EI69704</b>	Low Power VLSI Design					

**II M.Tech (Microelectronics & VLSI Design)****Semester-III**

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EI 6962/EI 69901	Dissertation Phase-I	-	-	20	-	20	-	-	100	150	250
<b>Total</b>			<b>0</b>	<b>0</b>	<b>20</b>	<b>0</b>	<b>20</b>	<b>0</b>	<b>0</b>	<b>100</b>	<b>150</b>	<b>250</b>

**Semester-IV (Admitted before 2012)**

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EI 6994	Dissertation Phase-II	-	-	30	-	30	-	-	160	240	400
Total			0	0	30	0	30	0	0	160	240	400

**Semester-IV (Admitted after & In 2012)**

S. No.	Sub. Code	Subject	L	T	P	Th. Credit	Pr. Credit	Maximum Marks				
								TH	CW	SW	Pr.	Total
1	EI 6996	Dissertation Phase-II	-	-	22	-	22	-	-	160	240	400
Total			0	0	22	0	22	0	0	160	240	400